



JRW

## AMENDMENT TRANSMITTAL LETTER

Docket No.  
SON-1285/DIV

Application No. 10/752,705-Conf. #5551	Filing Date January 8, 2004	Examiner Jack S. J. Chen	Art Unit 2813
---	--------------------------------	-----------------------------	------------------

Applicant(s): Takashi Noguchi, et al

FIELD EFFECT TRANSISITOR AND FABRICATION THEREOF, SEMICONDUCTOR  
Invention: DEVICE AND FABRICATION THEREOF, LOGIC CIRCUIT INCLUDING THE  
SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR SUBSTRATE

### TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	12	- 20 =	0	x 50.00	0.00
Independent Claims	3	- 3 =	0	x 200.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					.
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					0.00

Large Entity

Small Entity

No additional fee is required for this amendment.

Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.  
A duplicate copy of this sheet is enclosed.

A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.

Payment by credit card. Form PTO-2038 is attached.

The Director is hereby authorized to charge and credit Deposit Account No. 18-0013  
as described below. A duplicate copy of this sheet is enclosed.

Credit any overpayment.

Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

Dated: June 22, 2007

Ronald P. Kananeh  
Attorney/Agent Reg. No.: 24,104

RADER, FISHMAN & GRAUER PLLC  
1233 20th Street, N.W.  
Suite 501  
Washington, DC 20036  
(202) 955-3750



Docket No.: SON-1285/DIV  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

---

In re Patent Application of:

Takashi Noguchi, et al

Conf. # 5551

Application No.: 10/752,705

Group Art Unit: 2813

Filed: January 8, 2004

Examiner: Jack S.J. Chen

For: FIELD EFFECT TRANSISTOR AND  
FABRICATION THEREOF,  
SEMICONDUCTOR DEVICE AND  
FABRICATION THEREOF...

---

**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT (37 C.F.R. 1.121)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

This is in response to the Notice of Non-Compliant Amendment (37 C.F.R. 1.121) mailed on June 5, 2007.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 15 of this paper.